IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

pplicants : Ryutaro YAMANAKA et al.

Group Art Unit: 2133

Appl. No. : 10/748,242

Examiner: M.M. CHAUDRY

Filed

: December 31, 2003

Confirmation No.: 6967

For

: PROCESSING UNIT AND PROCESSING METHOD

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents U.S. Patent and Trademark Office Customer Service Window, Mail Stop AMENDMENT Randolph Building 401 Dulany Street Alexandria, VA 22314

Sir:

In accordance with the duty of disclosure under 37 C.F.R. § 1.56 and §§1.97-1.98, and supplemental to the Information Disclosure Statement filed on May 24, 2004, Applicants hereby call to the Examiner's attention the following documents:

"ARM Signal Processing Architecture (Piccolo) Architecture Reference Manual," ARM (January 1998);

"ARM Signal Processing Architecture (Piccolo) Product Guide," ARM (October 1996); and

"ARM Signal Processing Architecture (Piccolo) DSP Benchmarks," ARM (November 1996).

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Applicants respectfully request that the Examiner consider the above material and cite the same. Copies of the above-noted documents are attached hereto and all of the above-noted documents are listed on the attached PTO-1449 Form. The Examiner is requested to initial the appropriate spaces on the attached Form and to return a copy of the completed Form to Applicants with the next official communication in the present application.

Further, Applicants note that since this Supplemental Information

Disclosure Statement is being submitted subsequent to the issuance of an Office Action on the merits, Applicants have included herewith a check in the amount of \$180.00 to cover the fee set forth in 37 C.F.R. §1.17 (p). However, the Commissioner is authorized to charge payment of any necessary additional fees associated with this communication or credit any overpayment to Deposit Account No. 19-0089.

Should the Examiner have any questions, the Examiner is invited to contact the undersigned at the below-listed telephone number.

Respectfully Submitted, Ryutaro YAMANAKA et al.

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S. Department of Commerce Patent and Trademark Office

Atty. Docket no P24577

Application No. 10/748,242

Applicant

Ryutaro YAMANAKA et al.

BY APPLICANT
(Use several sheets if necessary)

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NFORMATION DISCLOSURE STATEMENT

Filing Date December 31, 2003 Group 2133

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	3	"ARM Signal Processing	Architecture (F	Piccolo) DSP Ben	chmarks," AF	RM (November	1996).				
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*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation

if not in conformance and not considered. Include copy of this form with next communication to applicant.